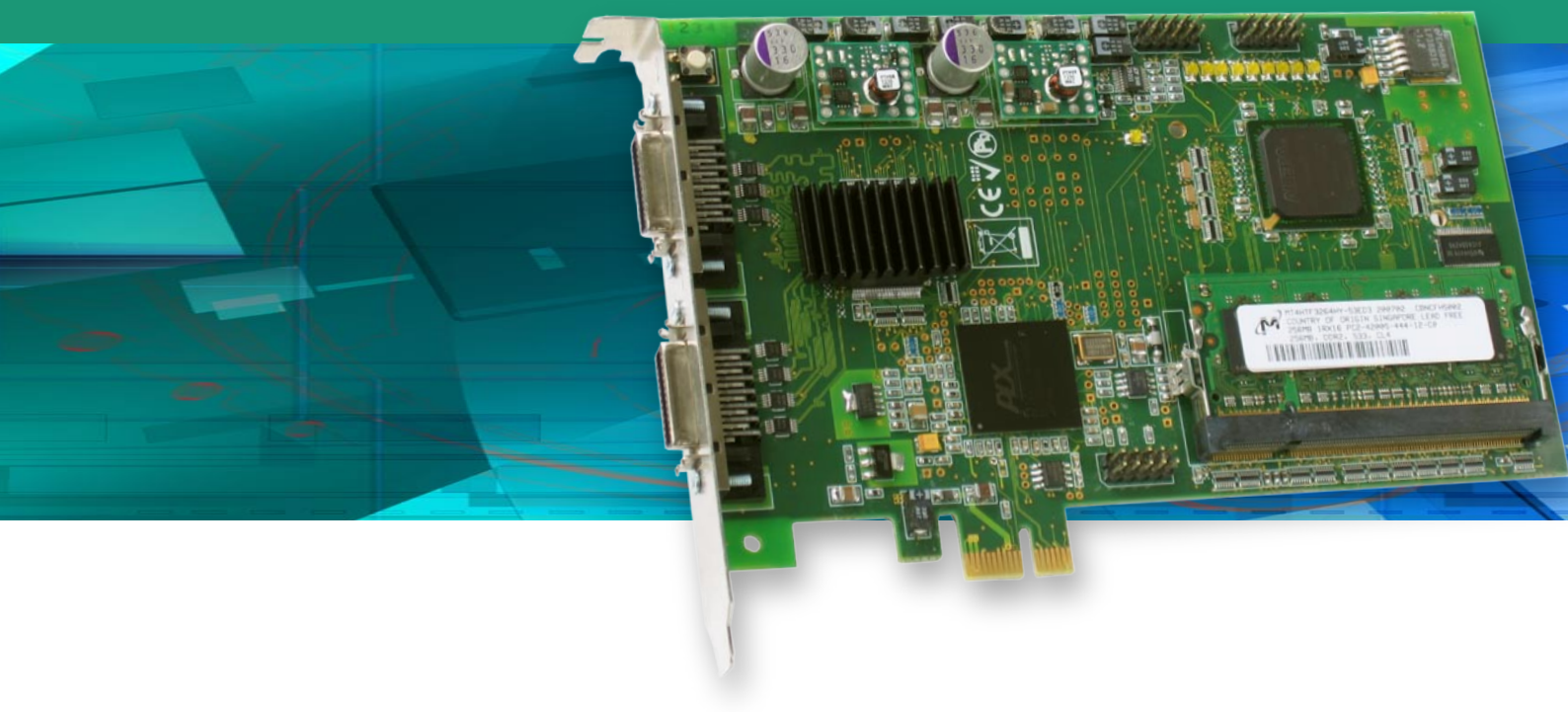
UFG-04 LVDS

30 bit high speed LVDS frame grabber



High Definition, High Pixel Depth

UFG-04 LVDS frame grabber enables the capture of high resolution LVDS video with the full 10 bits per color depth and with 270 Mpix/s pixel frequency. The extended on-board frame buffer enables the capture of frame-to-frame video clips regardless of the PC bus bottlenecks. The fast internal data pipelining and buffering makes sure that the PCIe™ interface is able to effectively transfer the image data.

Optimal for Testing Video Interfacing

UFG-04 LVDS is an optimal tool for testing the fidelity of input and scaler units for flat panel TVs and monitors. The LVDS input directly matchs the input of the modern flat panel LCD and PDP monitors replacing the actual display for testing. The flexible design of the internal data handling enables custom data input configurations and advanced on-board image functions.

With the native software library the application designer can effectively integrate the UFG-04 as a part of their system. By integration of the UFG-04 with Unigraf's VTG-5000 series video pattern generators the user can create an unique combination of most advanced video testing system available.

Benefits

- Captures images from High Definition LVDS sources
- 135 MHz Dual FPD-Link Receiver
- 10 bits per color color
- Compact half size PCI card
- High speed PCI-express bus
- Up to 512 Mpixels on-board capture buffer
- Sustained PCI bus data transfer speed up to 140 MBytes/s

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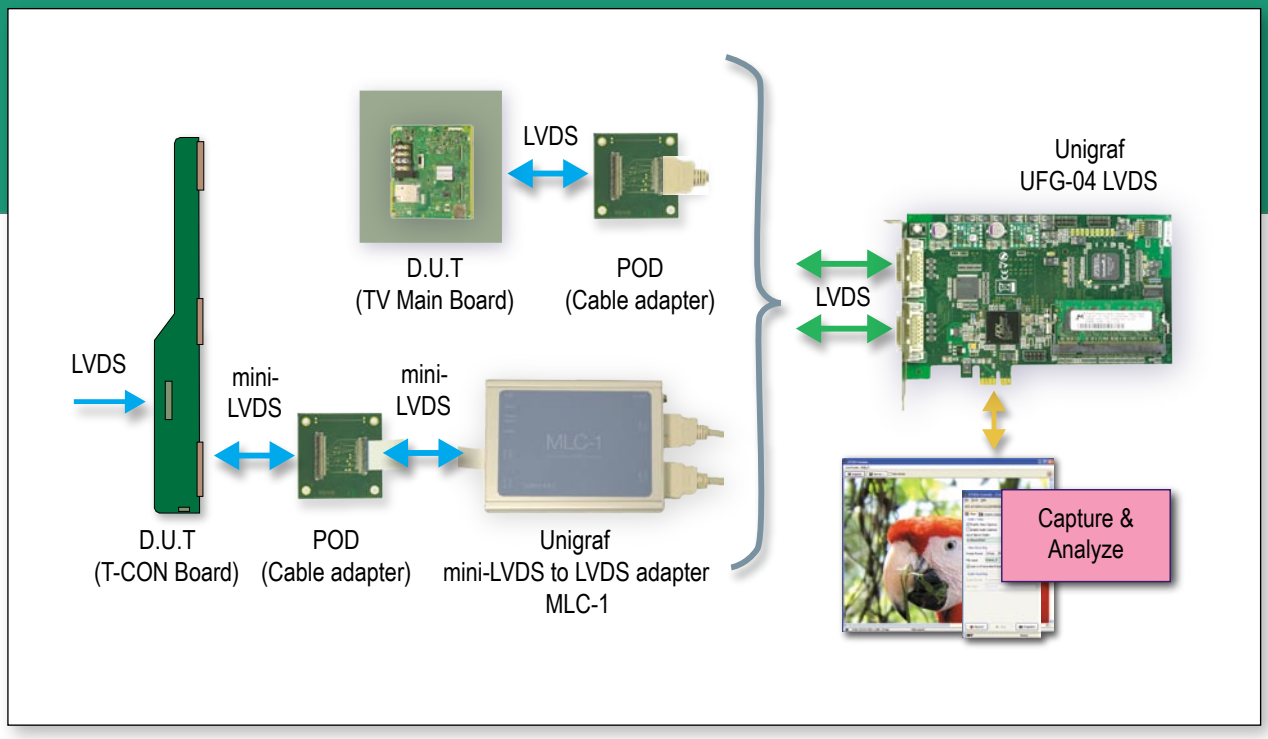

Video Interface Test

Test the output quality of your video input board within seconds. Measure each of the millions of pixels reliably every time. Analyze any test images with the accuracy that your quality criteria demands. Review each individual result in detail and include the long term trends into your quality reports.

Unigraf's unique Video Input Board Test System consists of a Unigraf VTG video pattern source and a Unigraf UFG video frame grabber. The combination provides flexible tools for creation of test functions and sequences to meet your video board testing needs.

Specifications

Inputs	2 x MDR-26 DISM 1.0 Dual FPD-Link Receiver (DS90C3202)
Pixel Frequency	
Single channel	135 Mpix/s max
Dual channel	270 Mpix/s max
Input Pixel Depth	6, 8 or 10 bits per color
Frame Buffer	2 or 4 GBytes
Data Interface	PCI Express 1 lane.
Operating Systems	Windows® 7 Vista or XP
SW Interface	API with full functionality to configure the board and capture video, audio, metadata and link status. Multi-board Support
Module Size	107 x 168 mm
Power Consumption	12 V: 7.5 W max; 3.3 V: 1.7 W max

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